

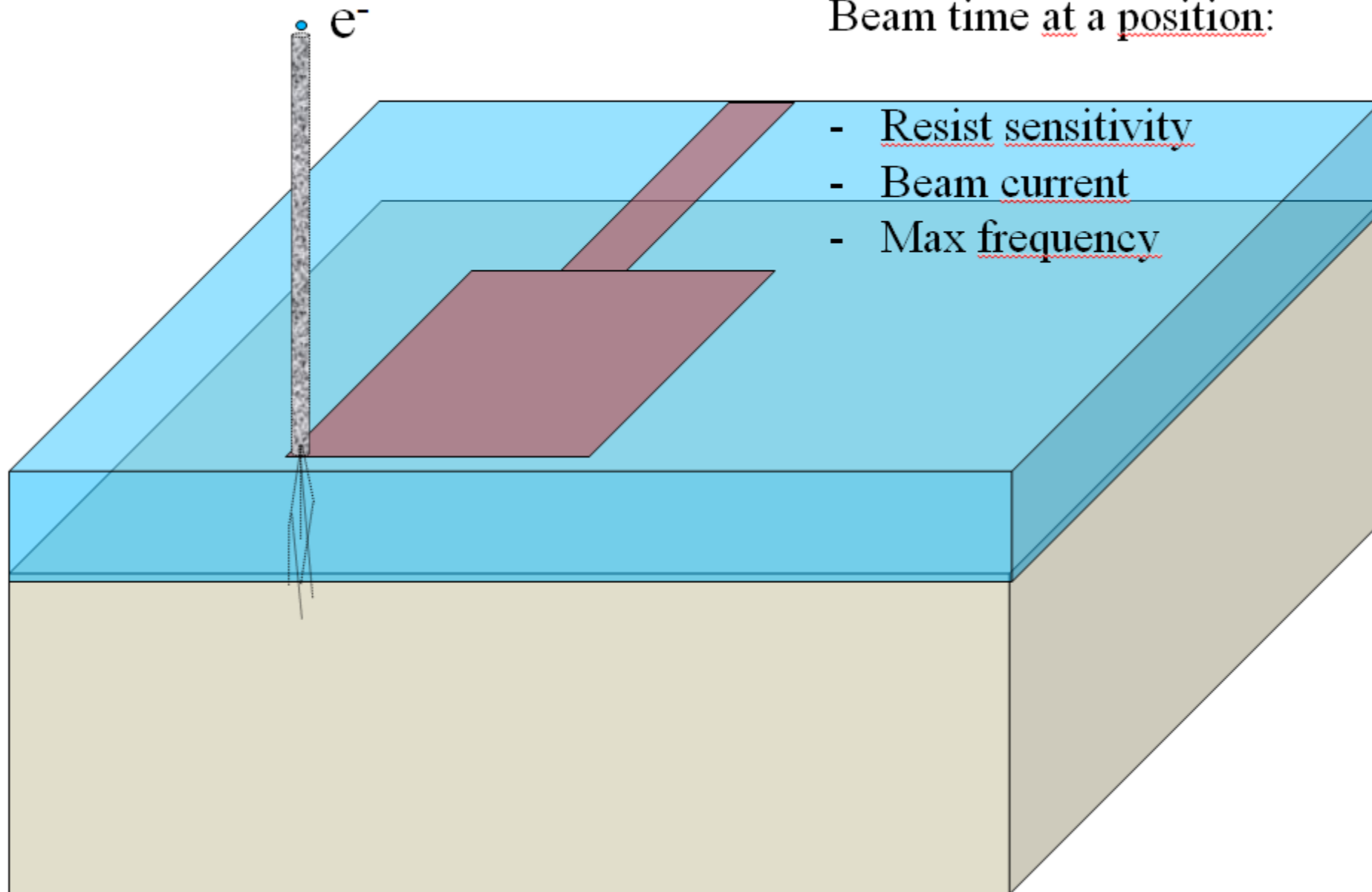
APPLICATIONS

Advanced exposure techniques

- Limitation of Electron Beam Lithography
- Beam Current / Beam Step Size
- PEC
- Summary

Gaussian Exposure Principle

Beam time at a position:



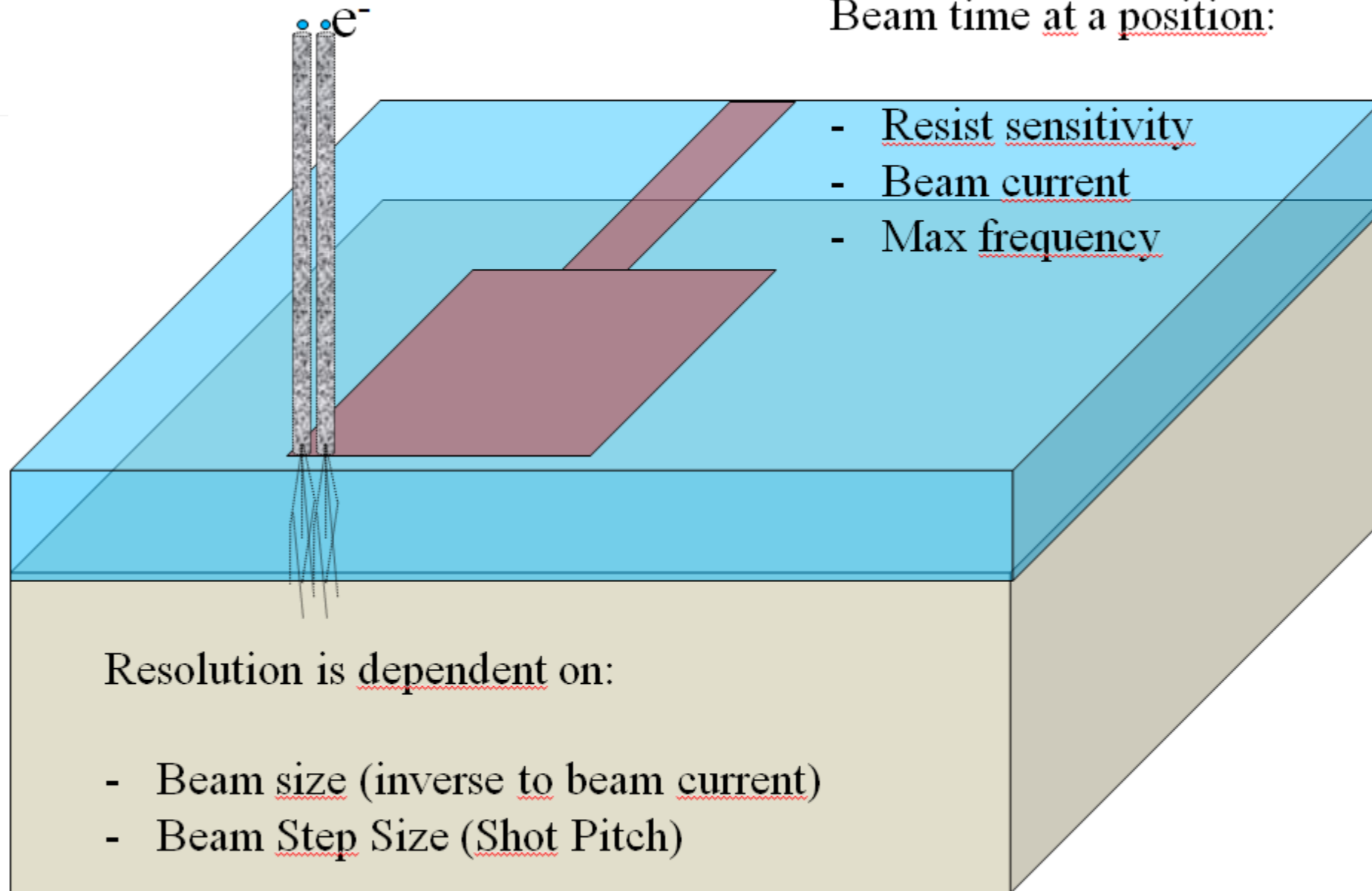
Gaussian Exposure Principle

Beam time at a position:

- Resist sensitivity
- Beam current
- Max frequency

Resolution is dependent on:

- Beam size (inverse to beam current)
- Beam Step Size (Shot Pitch)



- Beam on-time: $t = D * A / I$
 - i.e. $D=200\mu\text{C}/\text{cm}^2$, $A=1\text{cm}^2$,
 $I=1\text{nA}$ leads to 56 hours beam-on time!
- Best Performance calls for large currents
 - However, this increases the spot size, and thereby the CD sensitivity to dose
- Best Accuracy calls for small currents
 - ... and the write time goes to hell

where

$D = \text{dose } (\mu\text{C}/\text{cm}^2)$

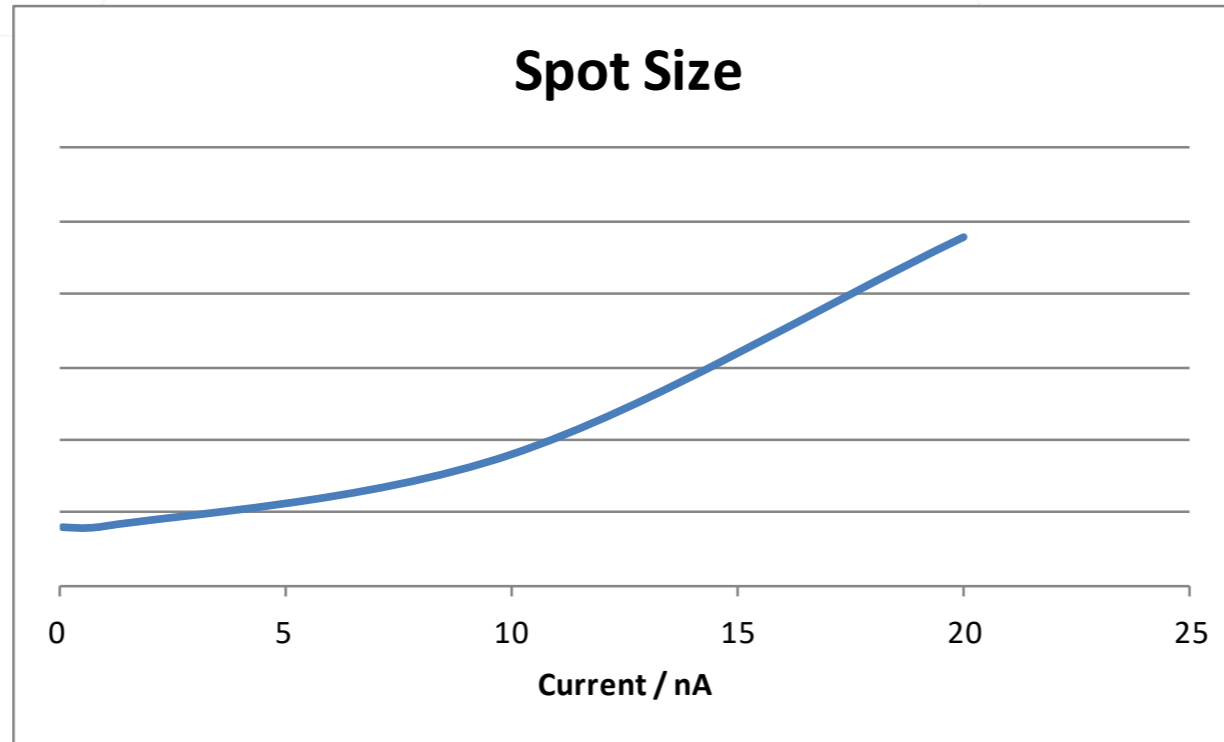
$I = \text{current (A)}$

$t = \text{time (sec)}$

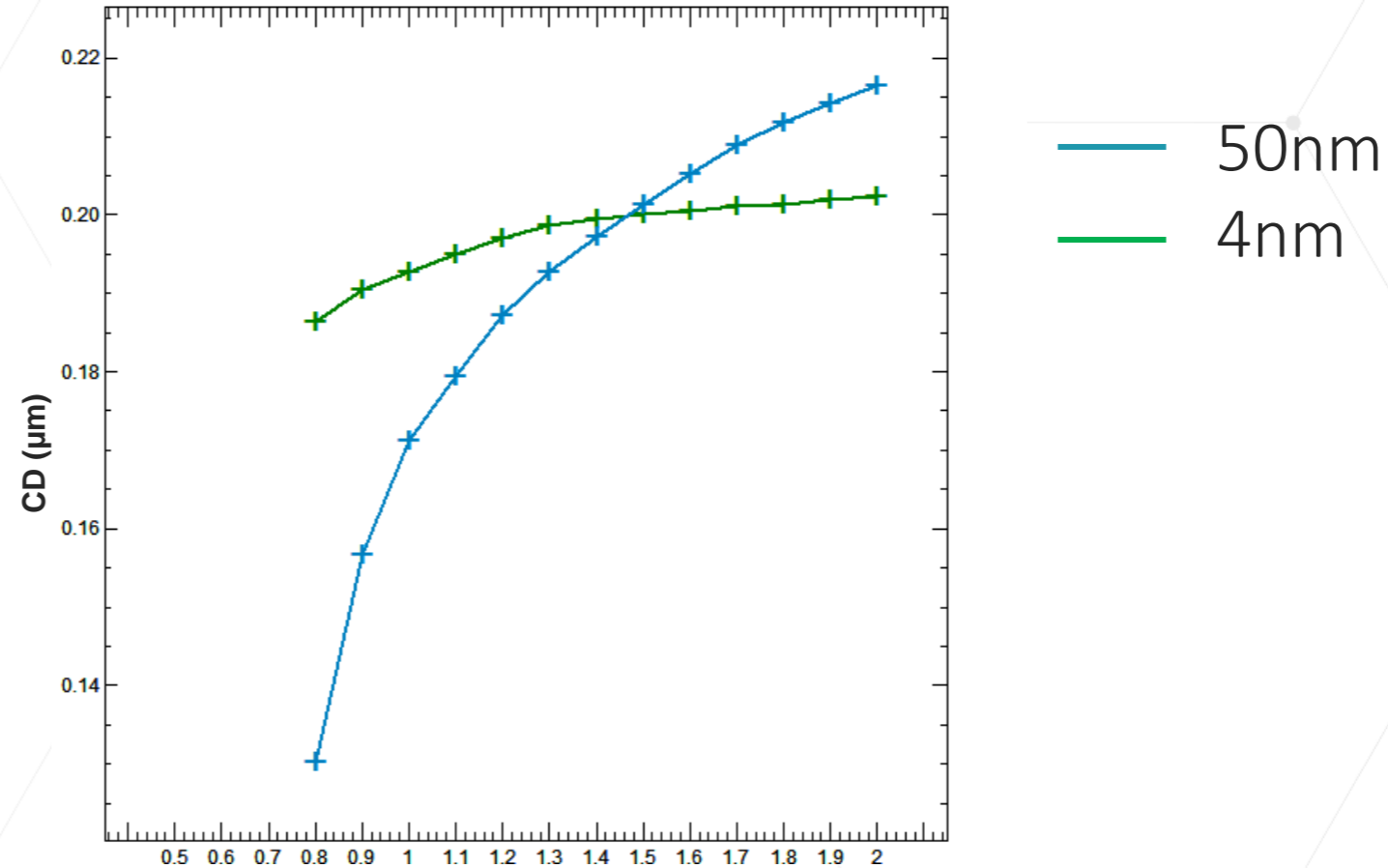
$A = \text{exposure area (cm}^2\text{)}$

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Spot size / current relationship

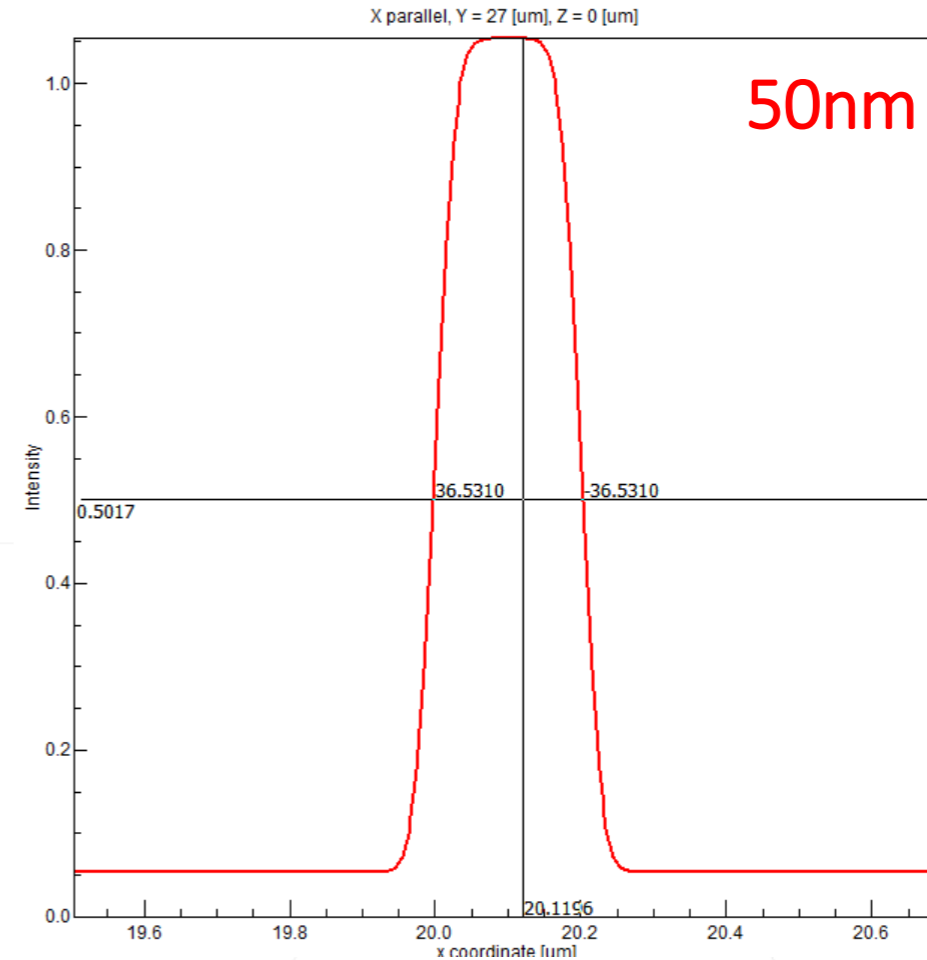
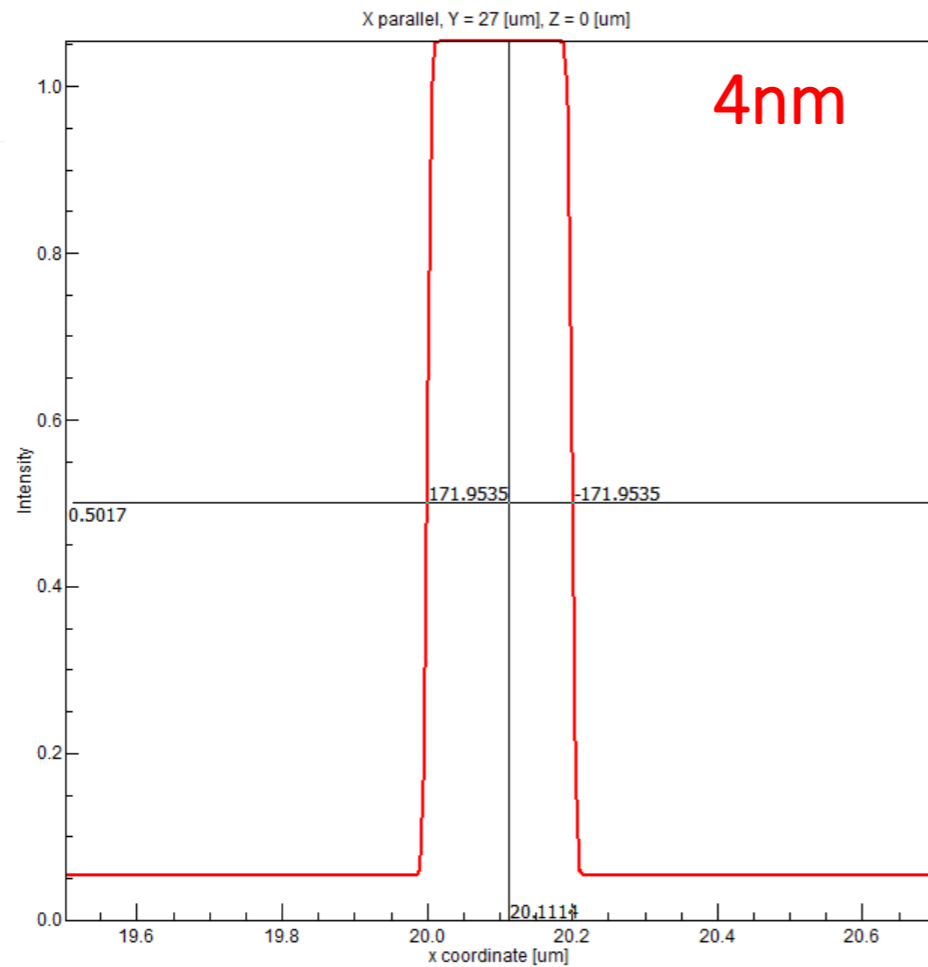


- Spot size increases at larger beam currents



- CD Sensitivity to Dose depends on spot size
 - 200nm line at spot sizes of 4nm and 50nm

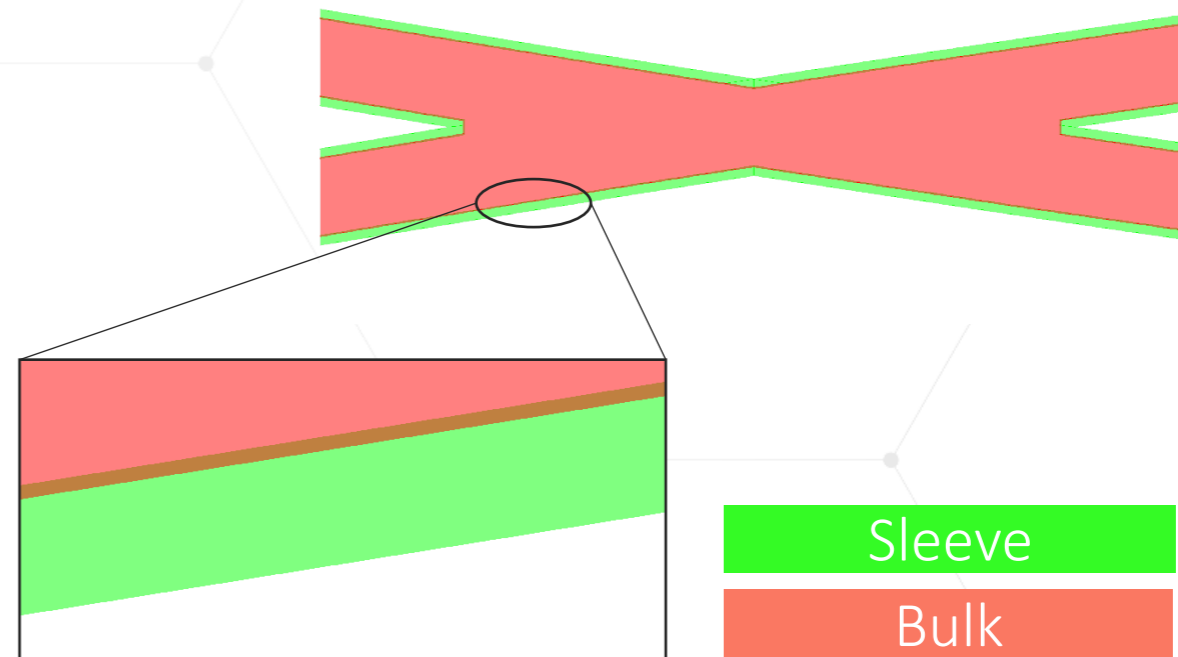
ILS and why this impacts the CD



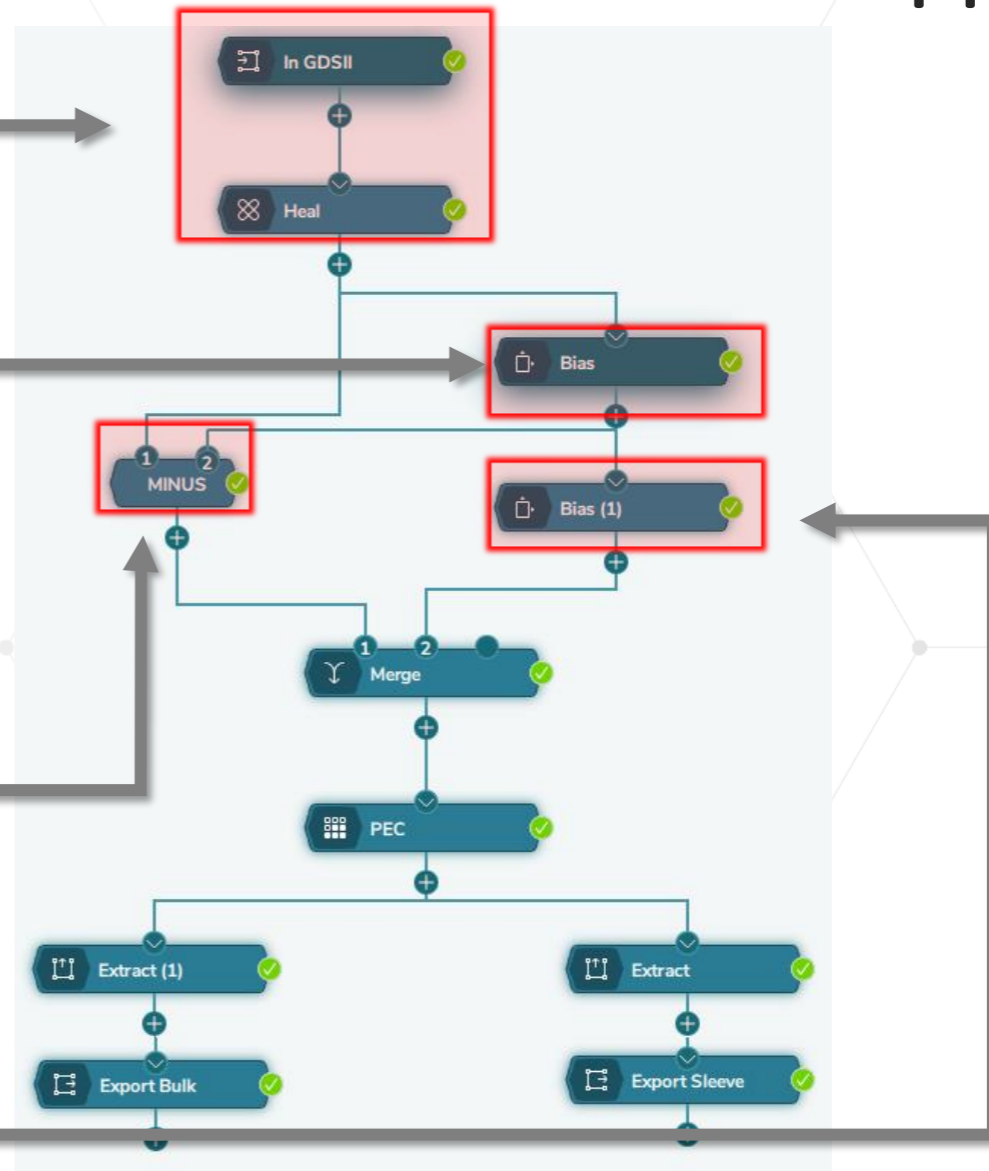
- Exposure Latitude 17.2 %/nm vs 3.65%/nm
 - -> Larger spot will show dose fluctuations more clearly

- Larger spot “magnifies” dose errors
 - Noise shows up stronger -> LER, LWR
 - Proximity Effect shows up stronger
- $\Delta CD = 22\text{nm}$ (50nm spot) vs. 4.6nm (4nm spot)
 - Proximity effect maximum dose error is $\eta / (1 + \eta)$
 - For $\eta = 0.7$, the maximum dose error is 41%
 - $\Delta CD = 2 * \text{DoseError} / \text{ILS}$

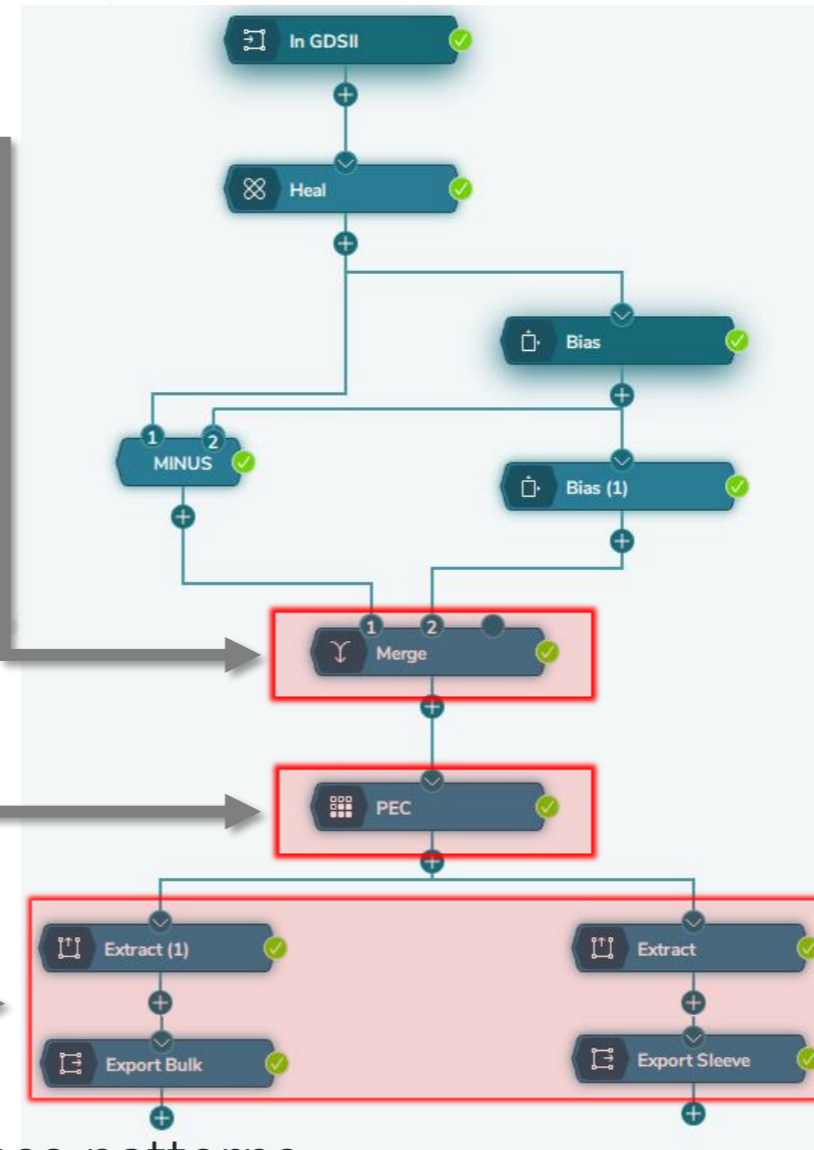
- Split layout to Bulk & Sleeve with core part (bulk) of the layout written with a large beam and step size and sleeve that is written with a small beam and step size.
- Two machine files will be generated for the exposure.



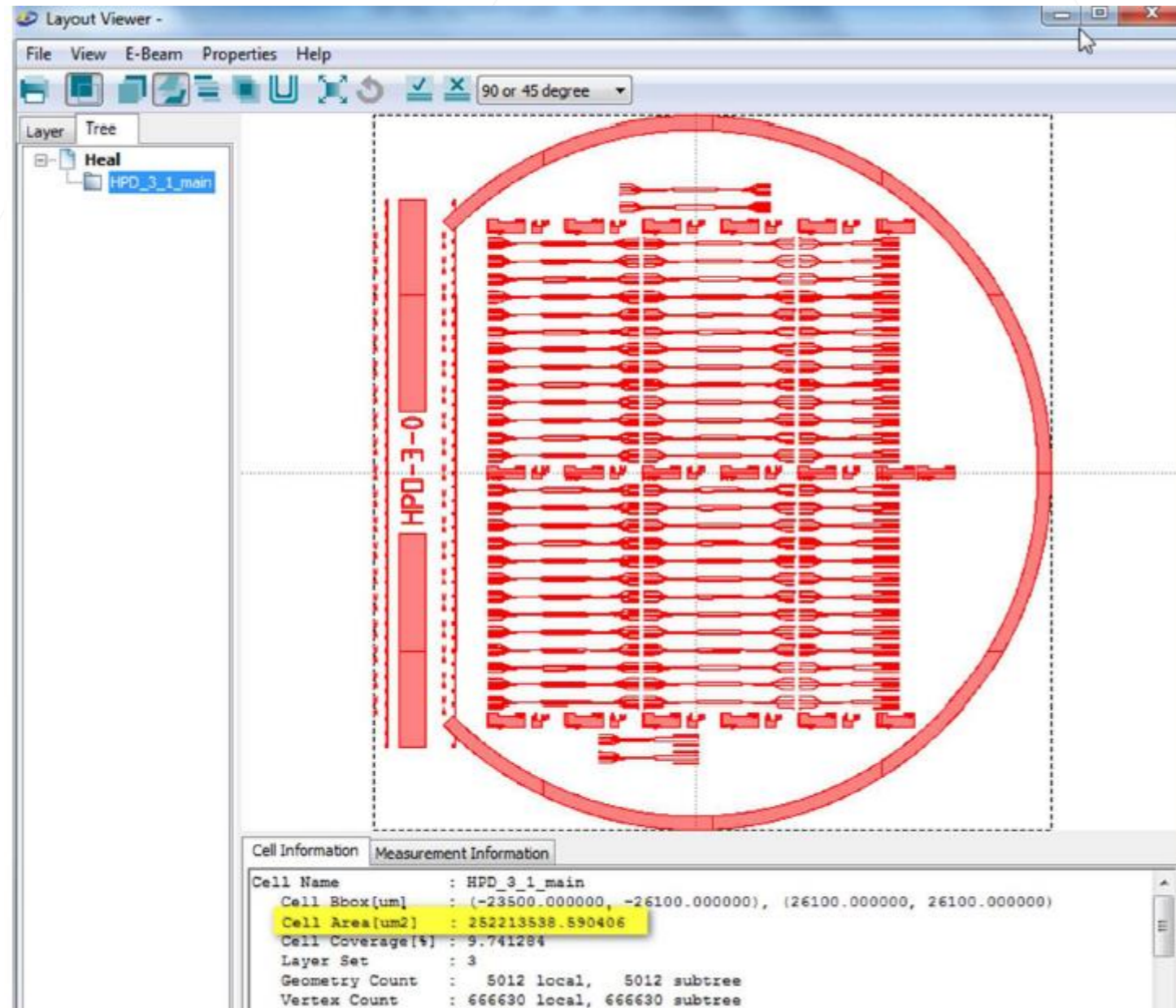
- Load and Heal
 - Provide a layout and remove any overlaps
- Create the bulk
 - Negatively bias the layout and create the bulk part
- Create the sleeve
 - Subtract from the original layout the bulk to get the sleeve
- Avoiding the gap
 - An one beam step size large overlap on the bulk will ensure that no openings appear in the final exposure result



- Merge the prepared layout
- Merging sleeve and bulk into one layout for PEC
- Performing dose correction
 - The dose correction is done on the combined bulk & sleeve layout taking their proximity into account
- Separate sleeve & bulk
 - The Extract and Export of the sleeve and the bulk allows the definition of different beam step sizes for each of these patterns.
 - By this approach the machine can write the pattern in minimal time



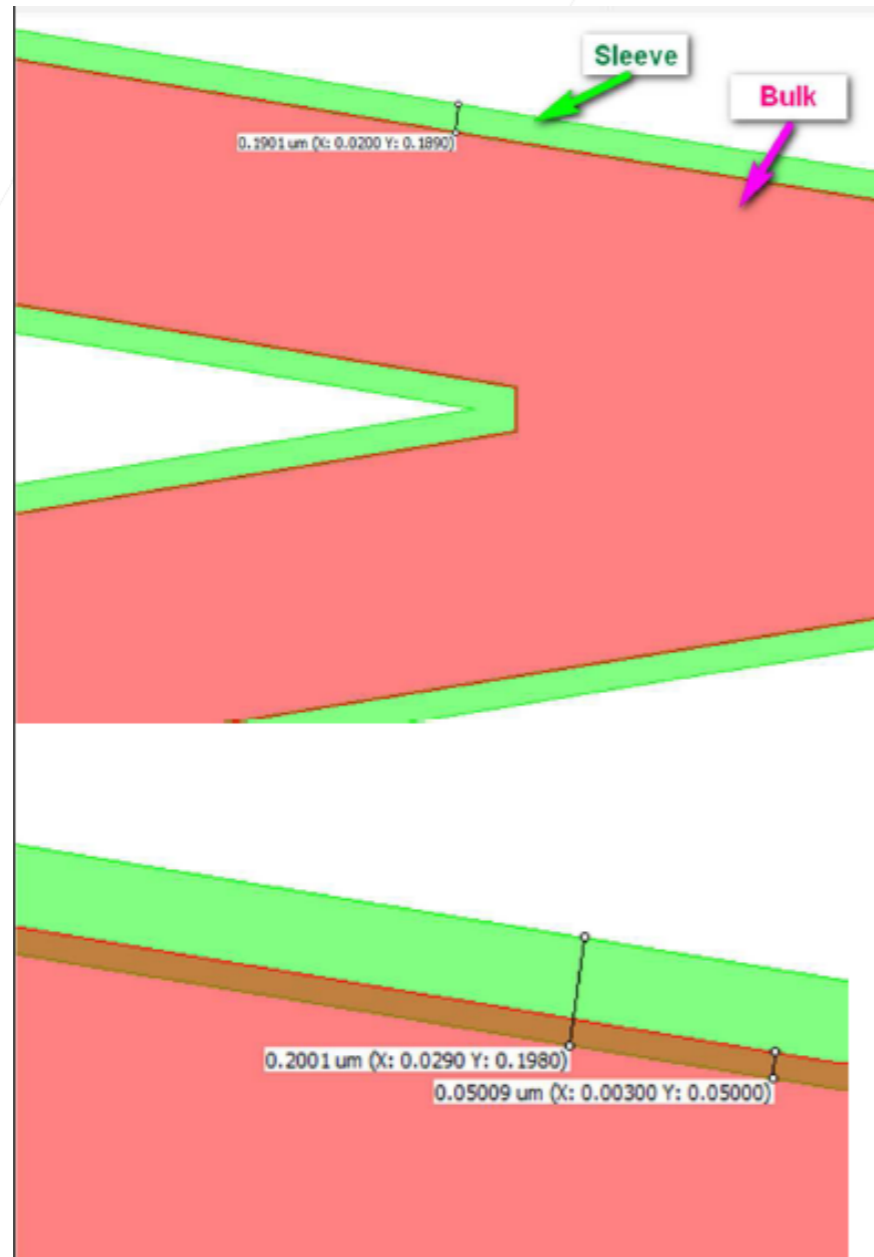
Example Waveguide Chip



Exposure Area:
 $252213538.590406 \mu\text{m}^2$
 $\sim 2.5 \text{ cm}^2$

Assume Dose required:
 $200 \mu\text{C} / \text{cm}^2$

Using 1nA for all
Exposure results in ~ 6
days writing time!



Exposure Area:
252213538.590406 μm^2
~ 2.5 cm^2

Slit Layout to **Sleeve** with area:
1115968.374040 μm^2
~ 0,01 cm^2

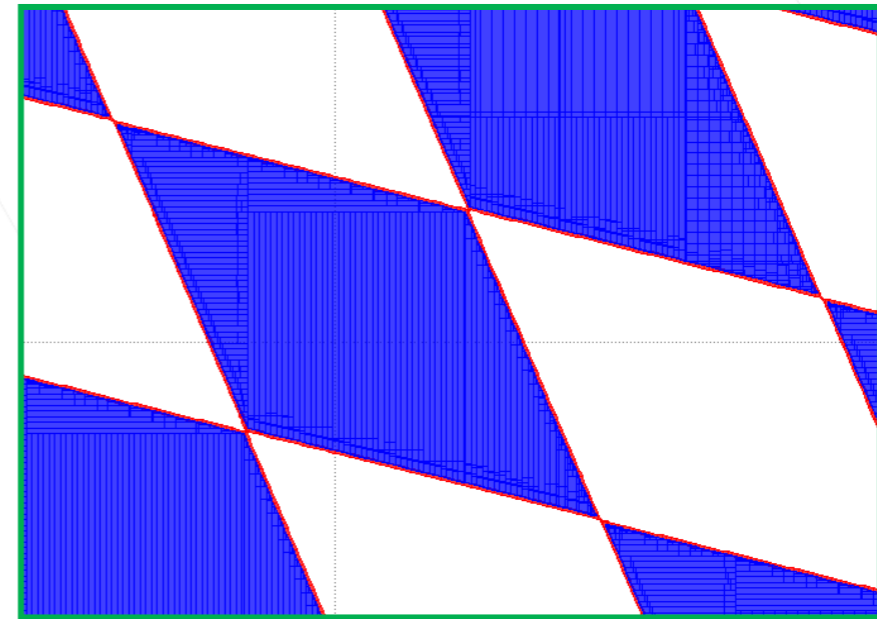
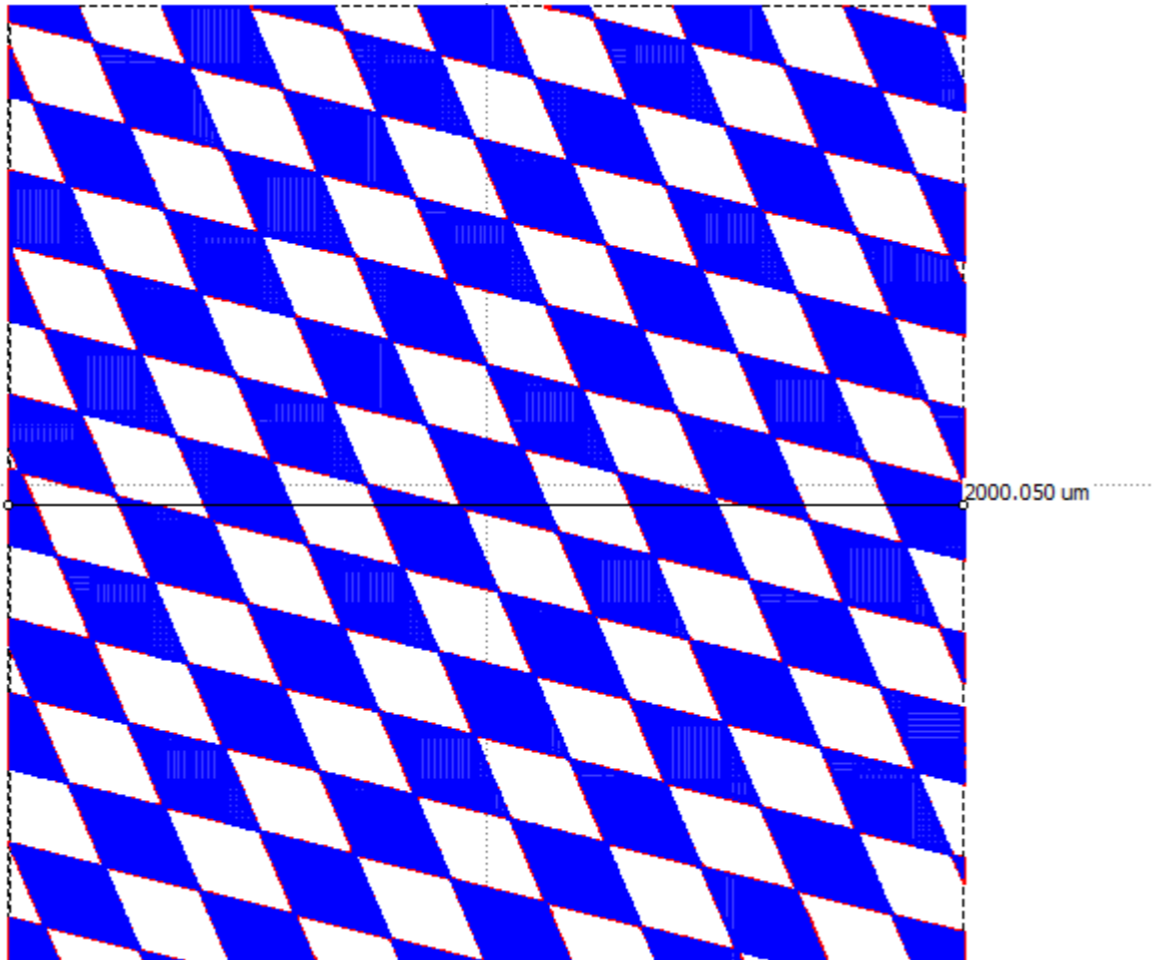
Expose with 1nA takes: ~ 30 min

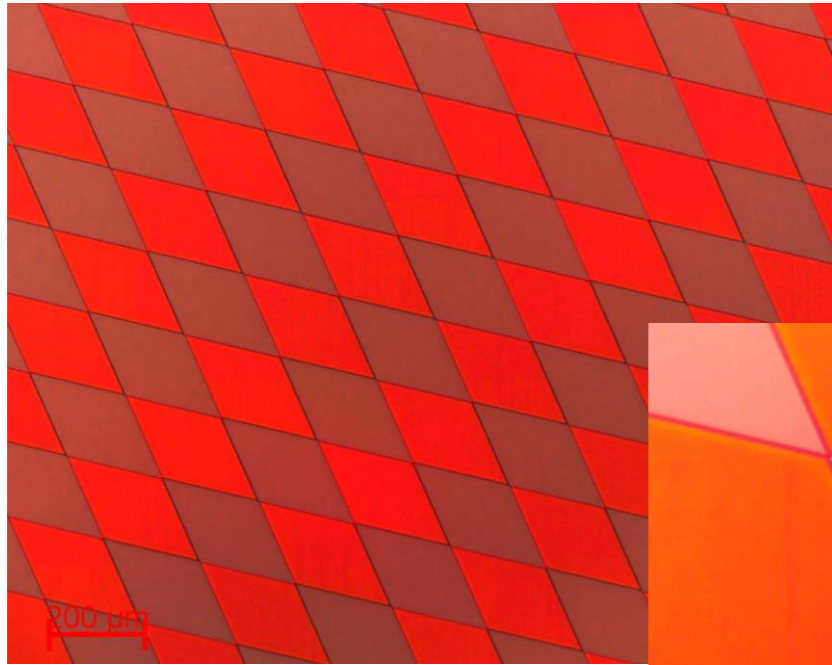
Expose Bulk with area:
251376605.100139 μm^2
~ 2,5 cm^2

Expose with 50nA takes 3 hours

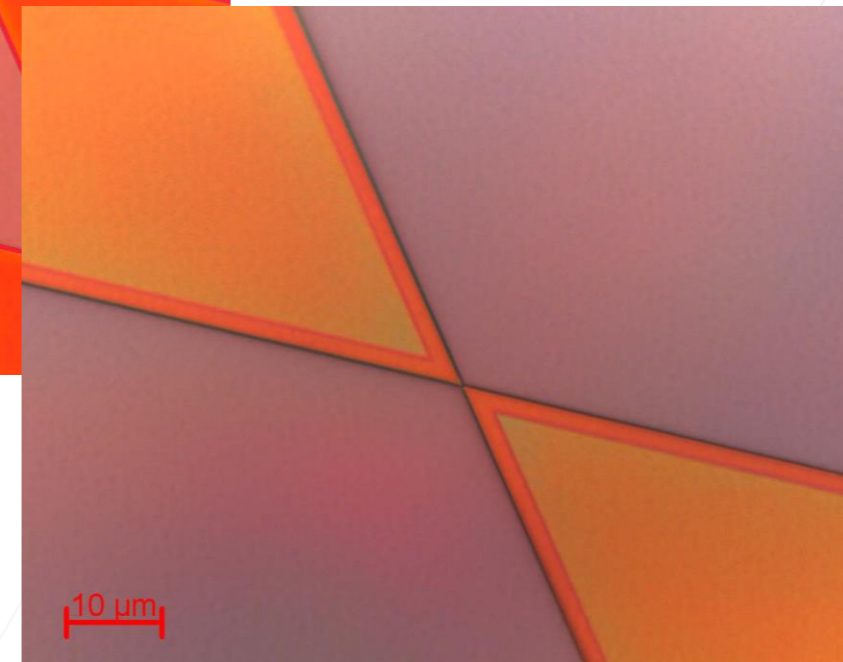
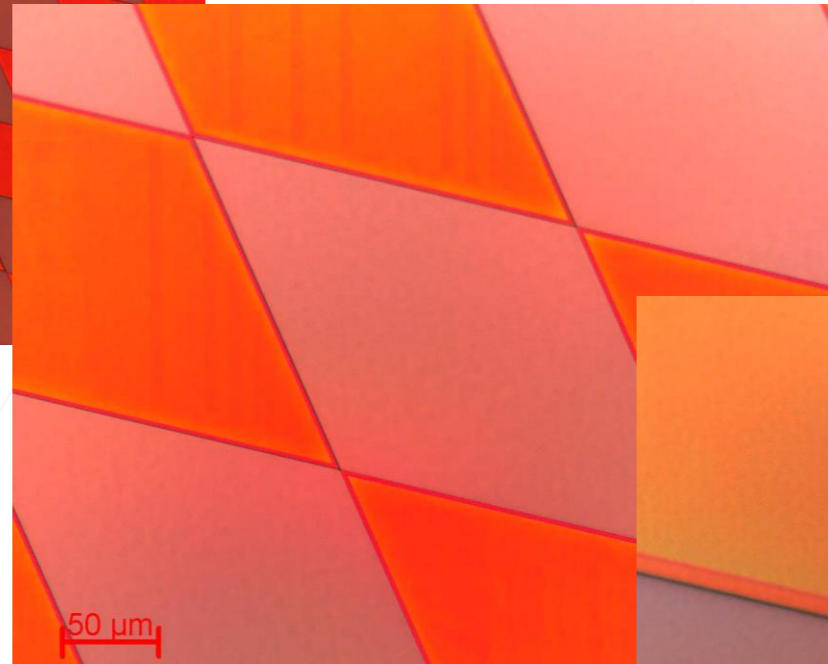
Total exposure including
switching current takes ~ 4 hour!

A 2000 μm x 2000 μm pattern was exposed using the Bulk & Sleeve method.





Exposure at Fraunhofer HHI – Berlin
Stack definition:
SAL601H on chromium/quartz mask blank
System: Vistec EBPG5000plus @ 50kV



Conditions:

bulk:

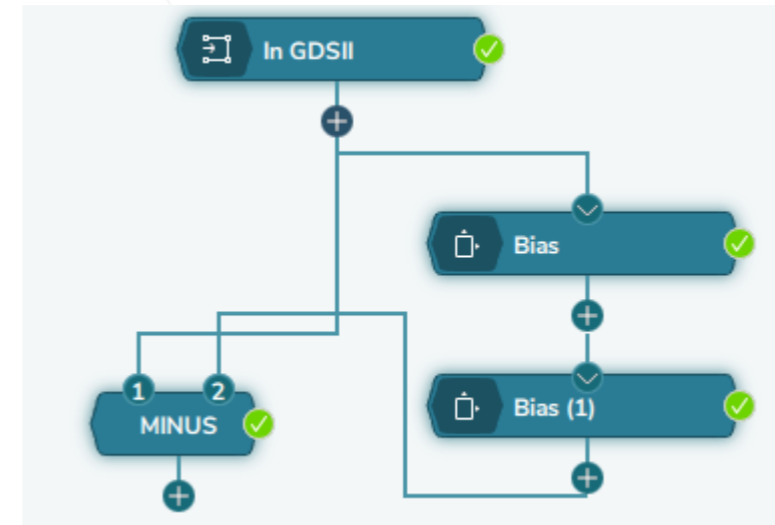
100nm resol, 20na cur, 10uC, 2xmultypass, 16.7MHz

sleeve:

25nm resol, 2na cur, 10uC, 2xmultypass, 30.6MHz

- Pattern exposure time without Bulk and Sleeve was about 2hours 30 Minutes
- Using Bulk & Sleeve with the PEC process reduced the time to about 25 minutes
- The total time saving is at 570%
- Keep in mind: we used two different exposure files so beam switching, calibration and such occur twice. This adds up and reduce the theoretical maximum gain.

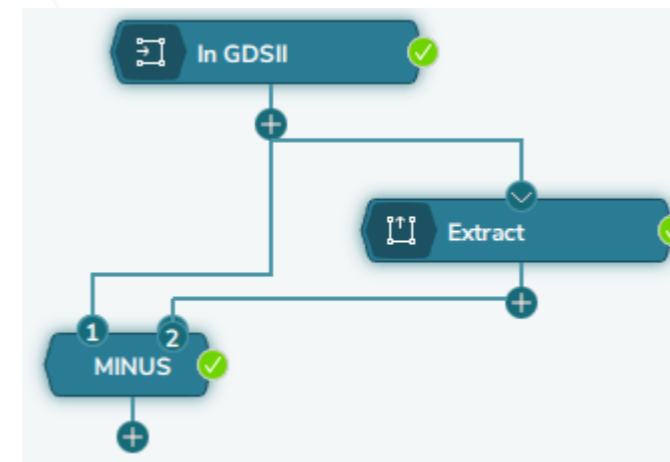
- Coarse - Fine Split



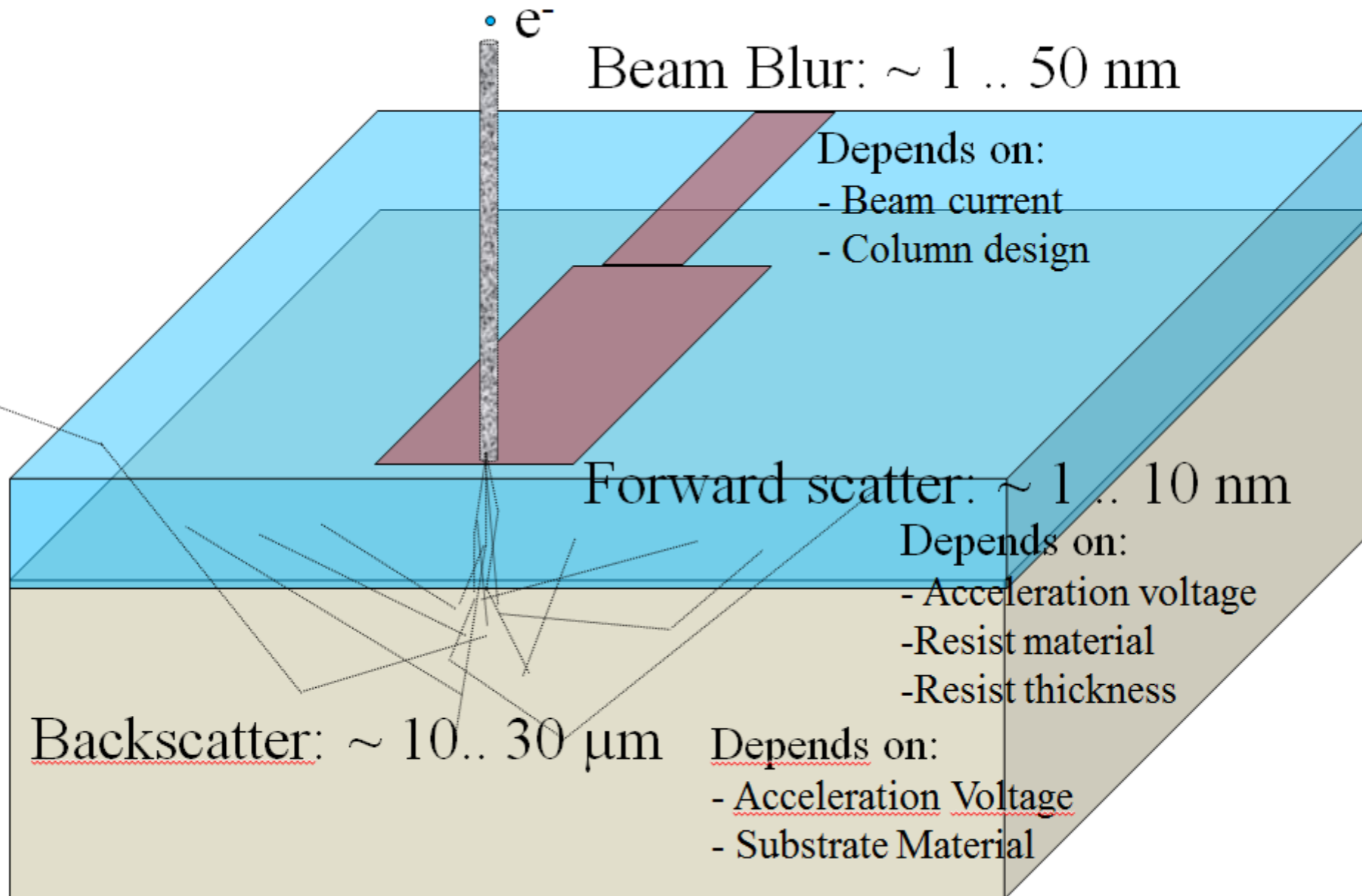
$< 1\mu\text{m}$

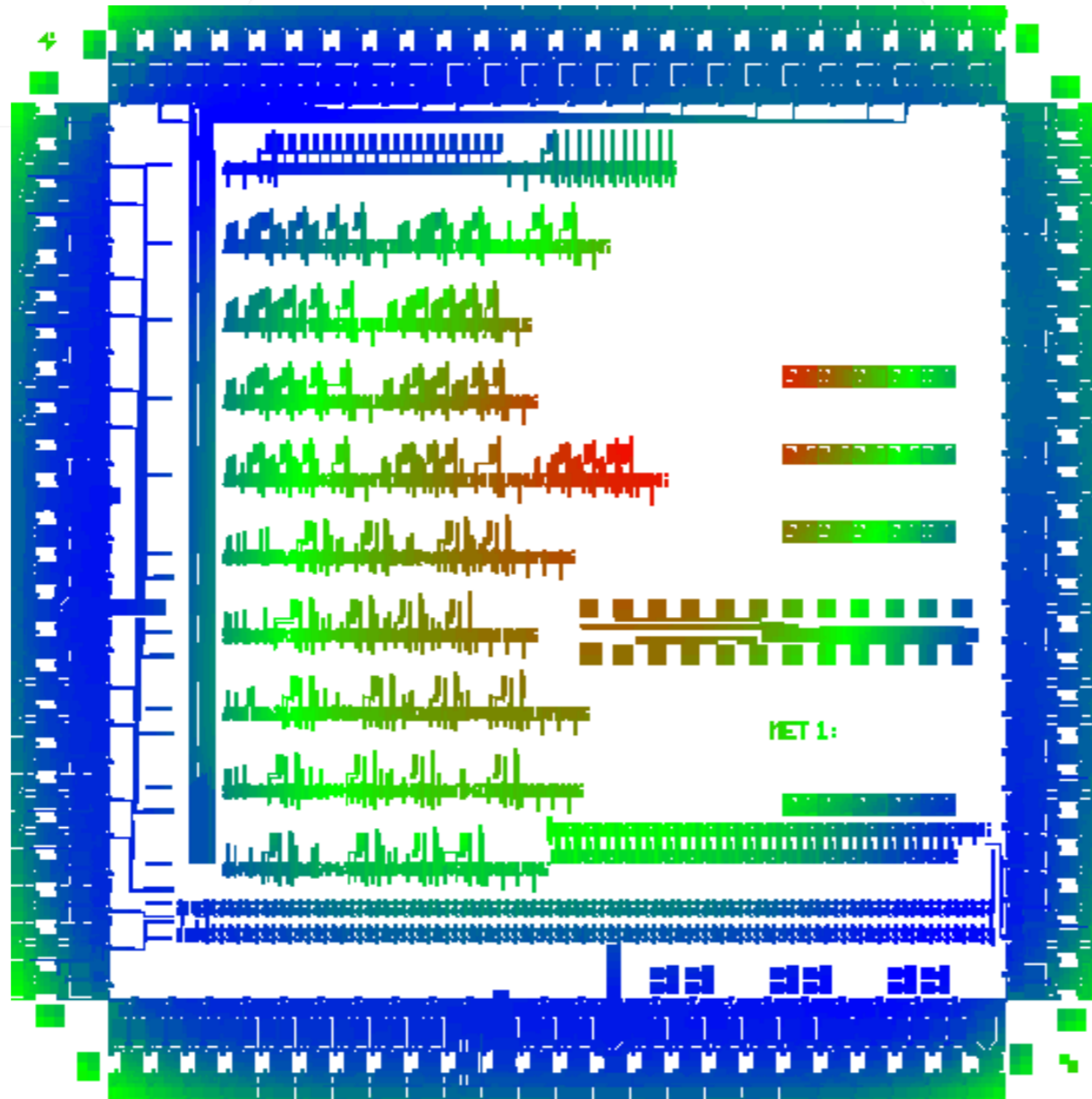
$> 1\mu\text{m}$

- Extract critical region or layer

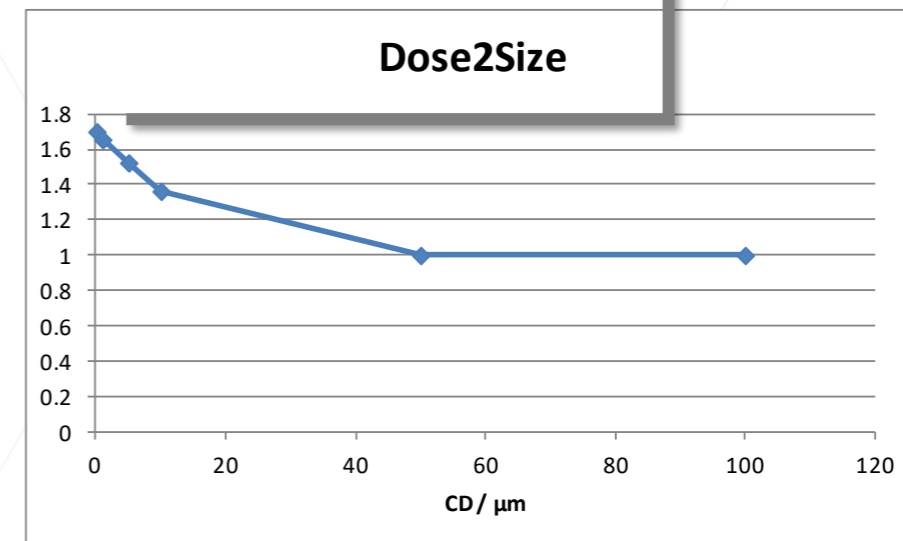
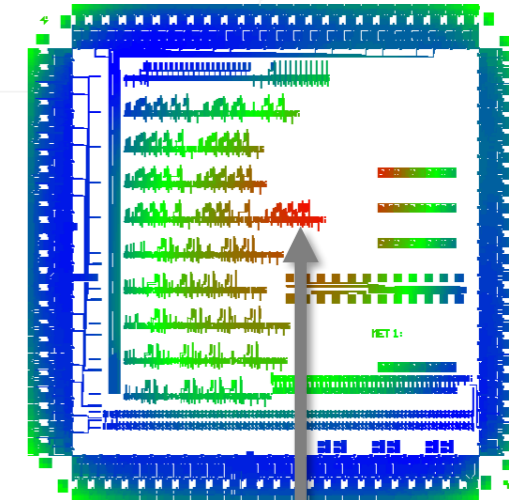


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- Improves accuracy, since iso-dense dose error is removed
- Improves write time
 - Without PEC, entire pattern has to be written at Dose-to-Size for small isolated pattern
 - Saves up to 50% write time in case 80% of the pattern consists of large features
 - Normalizes Dose
 - No need for dose variation



- Limitation of Electron Beam Lithography
- Beam Current / Beam Step Size
- PEC
- Summary

- Significant write time reductions through
 - Bulk-Sleeve - up to 90%
 - PEC – up to 50%
- Significant accuracy improvements through
 - Bulk-Sleeve
 - PEC
- If write time is an issue, use Bulk-sleeve, PEC

Thank You!

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